

PRELIMINARY DATA SHEET

RR88643-21 MMMB Power Amplifier Module

Application and Features:

- Support LTE, WCDMA, CDMA, TDSCDMA applications
- CDMA Bands BC0, BC1, BC4, BC6, BC10, BC15
- WCDMA Bands I, II, III, IV, V, VIII, IX
- TD-SCDMA Bands 34, 39
- FDD LTE Bands 1, 2, 3, 4, 5, 7, 8, 9, 12, 13, 17, 20, 25, 26, 28, 30
- TDD LTE Band 34, 38, 39, 40, 41
- Integrated Input LB Switch
- Integrated HB 2RX Switch
- Optimized for APT DCDC operation
- Support MIPI RFFE
- Compact Low Profile Package Dimension
 - 4.0 x 6.8 x 0.75 mm
 - 42-pad configuration

Block Diagram:

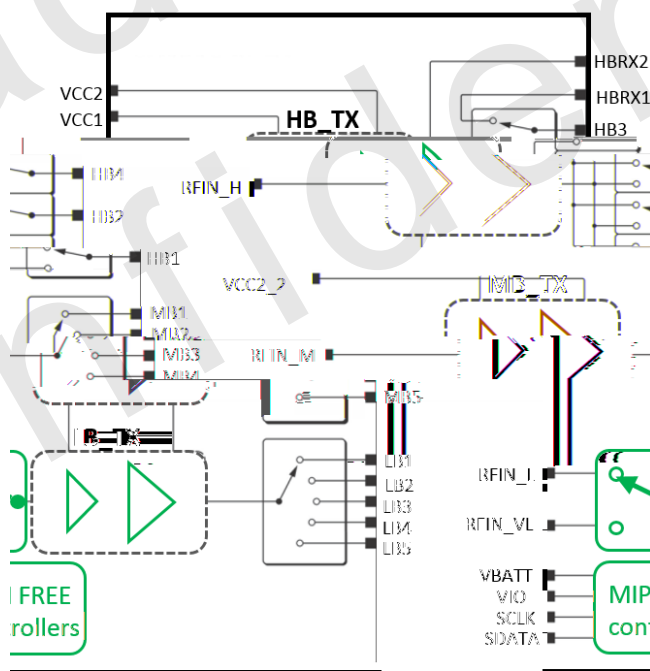


Figure 1. RR88643 Functional Block Diagram

Electrical Spec:

The following table shows the RR88643 absolute max condition.

Table 1. RR88643 Absolute Max Conditions

Parameter		Label	Min	Typical	Max	Unit
RF Input Power		PIN		0	15	dBm
Supply Voltage	No RF	VBATT		3.8	6.0	Volts
		VCC1, VCC2,		3.4	6.0	
	With RF	VBATT		3.8	6.0	
		VCC1, VCC2,		3.4	5.5	
Digital Control Signal		VIO, SCLK, SDATA			2	Volts
Temperature	Operating	TCASE	-40	25	+100	°C
	Storage	TSTG	-40		+150	
HBM ESD		ESD_HBM	-1		+1	kV

1 Exposure to Max rating conditions for ETCended periods may reduce device reliability. There is no damage to device with only one parameter set at the limit and all other parameters set at or below their Typical value.

2 Case Operating Temperature (TCASE) refers to the temperature of the GROUND PAD at the underside of the package.

Table 2. RR88643 Recommended Operating Conditions

Parameter		Label	Min	Typical	Max	Unit
Supply Voltage		VCC1	0.55	3.4	4.8	Volts
		VCC2	0.55	3.4	4.8	
		VBATT	3.0	3.8	4.8	
MIPI RFFE Supply		VIO	1.65	1.8	1.95	Volts
MIPI RFFE Signal Voltage for SCLK, SDATA	Low	VMIPI_LOW	0	0	0.2xVIO	Volts
	High	VMIPI_HIGH	0.8xVIO	1.8	VIO	
Leakage Current	VBATT = 3.4 V	I_BATT_LK			10	µA
	VCC1, VCC2, VCC2 = 3.4 V	I_CC_LK			10	
Case Operating Temperature Range		TRANGE	-40	+25	+90	°C

Table 3-1. MIPI RFFE Standard Register Map

Register Address	Register Name	Bit Position	Description	Default	Notes	Trigger Support	R/W
0x00	PA Mode Control	[7]	Trigger Select	0	0 = Trigger 0,1,2 or'd together 1 = Trigger 0,1,2 fire independently	Trigger 0	R/W
		[6:3]	PA Band Select Control Mode	0000	0000 = PA's Disabled 0001 = LB1_TX 0010 = LB2_TX 0011 = LB3_TX 0100 = LB4_TX 0101 = LB5_TX 0110 = MB1_TX 0111 = MB2_TX 1000 = MB3_TX 1001 = MB4_TX 1010 = MB5_TX 1011 = HB1_TX 1100 = HB2_TX 1101 = HB3_TX 1110 = HB4_TX 1111 = PA's Disabled (High Switch Isolation)		
		[2]	PA Enable	0	0 = PA OFF 1 = PA ON		
		[1]	Power Mode	0	0 = HPM 1 = LPM		
		[0]	LB Input Switch	0	0 = RFIN_L (Default for MHB Operation) 1 = RFIN_VL		
0x01	Primary PA Bias	[7:4]	PA Stage 2 Bias	0000		Trigger 0	R/W
		[3:0]	PA Stage 1 Bias	0000			
0x02	HB Switch RX Control	[7:4]	Reserved	00	Reserved	Trigger 0	R/W
		[3:0]	HB Switch RX Control	0000	0000 = Switch Off (Standby) 0001 = HB1 -> HBRX2 0010 = HB2 -> HBRX2 0011 = HB3 -> HBRX1 0100 = HB4 -> HBRX2 Other States = High Switch Isolation		
0x03	Bias Control	[7]	Bias Mode	0	0 = Limited Bias Mode 1 = Standard Bias Mode	Trigger 0	R/W
		[6:4]	Reserved	0	Reserved		
		[3:0]	PA Boost Stage Bias (IBST)	0000	Details in Bias Table 1		
0x04	Reserved	[7:0]	Reserved	0	Reserved	Trigger0	R/W
0x05	Reserved	[7:0]	Reserved	0	Reserved	Trigger0	R/W
0x06	Reserved	[7:0]	Reserved	0	Reserved	Trigger0	R/W
0x1C	PM_TRIG	[7:6]	PWR_MODE	10	00 = Normal Operation (Active) 10 = Low Power 01 = Default Settings (Startup) 11 = Reserved	NO	R/W
		[5]	Trigger Mask 2	0	0 = Trigger Enable 1 = Trigger Disable		
		[4]	Trigger Mask 1	0	0 = Trigger Enable 1 = Trigger Disable		
		[3]	Trigger Mask 0	0	0 = Trigger Enable 1 = Trigger Disable		
		[2]	Trigger Register 2	0	1 = Latch Register 2 Contents		
		[1]	Trigger Register 1	0	1 = Latch Register 1 Contents		
		[0]	Trigger Register 0	0	1 = Latch Register 0 Contents		
0x1D	Product ID	[7:0]	Product ID	00101101	Product ID = 0x2D	NO	R
0x1E	Manufacture ID	[7:0]	Manufacture ID [7:0]	11110011	Manufacture ID = 0x3F3	NO	R
0x1F	User ID	[7:6]	Reserved	00		NO	R/W
		[5:4]	Manufacture ID [9:8]	11	Manufacture ID = 0x3F3		R
		[3:0]	USID	1111	USID=0x0F		
0x20	EXT_Product ID	[7:0]	Extend Product ID	00000000	EXT_product ID = 0x00	NO	R

LB Electrical Spec:

Table 4. RR88643 Electrical Specifications for NTC – Transmit WCDMA Low Band

VBATT = 3.4 V; TCASE = +25 °C; Voice RMC 12.2 kbps

Parameter	Label	Conditions	Min	Typ	Max	Units	
Frequency	Band 5	f_o	824		849	MHz	
	Band 8		880		915		
Max Output Power	Bands 5, 8	Prated_NTC	29			dBm	
		Prated_ETC	$V_{BATT} = V_{CC} = 3.0 V, T_{CASE} = T_{RANGE}$	28.5			
Power Gain		GP_NTC	$P_{OUT} = Prated$	27.5	32.0	dB	
		GP_ETC	$P_{OUT} = Prated, T_{CASE} = T_{RANGE}$	26.5	33.0		
		GP_LOW	$V_{CC} = 0.55 V, P_{OUT} = 3 dBm,$		18	20.0	dB
Power Added Efficiency ¹	PAE_APT	$p_{out} = Prated$		42		%	
Total Supply Current ²	I _{total_MAX}	$p_{out} = Prated, V_{BATT} = 3.8 V$				mA	
Adjacent Channel Leakage power Ratio	5 MHz offset	ACLR1	$P_{OUT} = Prated$		-39	-36	dBc
			$P_{OUT} = Prated_ETC, V_{BATT} = V_{CC} = 3.0 V, T_{CASE} = T_{RANGE}$			-36	
	10 MHz offset	ACLR2	$P_{OUT} = Prated$		-50	-48	
			$P_{OUT} = Prated_ETC, V_{BATT} = V_{CC} = 3.0 V, T_{CASE} = T_{RANGE}$			-46	
Modulation Accuracy	EVM	$V_{BATT} = 3.0 V \text{ to } 4.8 V, Load = 50 \text{ ohms}, T_{CASE} = T_{RANGE}$		2.5	5.0	%	
Harmonics	Second	$2f_o$	P_{OUT}		-15	-13	dBm
	Third	$3f_o$			-21	-17	
	Fourth and higher	$4f_o$				-20	
Noise Power in Rx Band at Duplex Frequency with WCDMA Modulated Tx							
B5 $f_{TX} = 824\text{--}849 \text{ MHz}$	P _{NOISE_DPX}	$f_{RX} = f_{TX} + 45 \text{ MHz}$		-132		dBm/Hz	
B8 $f_{TX} = 880\text{--}915 \text{ MHz}$		$f_{RX} = f_{TX} + 45 \text{ MHz}$		-132			
Input VSWR	VSWR_IN	P_{OUT}		1.6:1	2:1	VSWR	
Stability	S	No oscillations, all spurious < -36 dBm, P_{OUT} $BATT = 3.0 V \text{ to } 4.8 V, T_{CASE} = T_{RANGE}$	6:1			VSWR	
Ruggedness	Ru	No permanent damage to module. P_{OUT} operation, $V_{BATT} = V_{CC} = 4.8 V, T_{CASE} = +25 \text{ °C}$	10:1			VSWR	

¹ V_{CC} optimized for ACLR1 = -40 dBc.

² $I_{TOT} = I_{BATT} + (I_{CC1} + I_{CC2})(V_{CC}/V_{BATT})(1/DC_DC_EFF)$. $V_{CC} \sim 2.9 V$. $DC_DC_EFF \sim 96\%$.

Table 5-0. RR88643 Electrical Specifications for NTC – Transmit LTE Low Band

VBATT = 3.4 V; TCASE = +25 °C; LTE Signal = QPSK/10 MHz/12RB for MPR = 0 and QPSK/20 MHz/100RB for MPR = 1.

Parameter		Label	Conditions	Min	Typ	Max	Units
Frequency	Band 5	f0		824		849	MHz
	Band 8			880		915	
	Band 12			699		716	
	Band 13			777		787	
	Band 17			704		716	
	Band 20			832		862	
	Band 26			814		849	
	Band 28			703		748	
Max Output Power	Bands 20, 28	Prated		28			dBm
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	26.5			
	Bands 5, 8, 12, 13, 17, 26	Prated		28			
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	26.5			
Power Gain	Bands 12, 13, 17, 28	GP_NTC	POUT = Prated	28.0		32.0	dB
		GP_ETC	POUT = Prated, TCASE = TRANGE	27.0		33.3	
	Bands 5, 8, 20, 26	GP_NTC	POUT = Prated	27.5		32.0	
		GP_ETC	POUT = Prated, TCASE = TRANGE	26.5		33.5	
	GP_LOW	VCC = 0.55 V, POUT = 3 dBm			20.0	dB	
Power Added Efficiency ¹	Band 12, 17, 28	PAE_APT	Prated		35		%
	Bands 5, 8, 13, 20, 26				35		
Total Supply Current ²		I_TOT_MAX_B12,17	POUT = 27.0 dBm, VBATT = 3.8 V				mA
		I_TOT_MAX_B5,8,13,26					
		I_TOT_MAX_B28	POUT = 27.5 dBm, VBATT = 3.8 V				
		I_TOT_MAX_B20					
Adjacent Channel Leakage power ratio	EUTRA	EUTRA_ACLR1	POUT = Prated		-38	-36	dBc
			POUT = Prated_ETC, VBATT = VCC = 3.0 V, TCASE = TRANGE			-33	
	UTRA1	UTRA_ACLR1	POUT = Prated		-40	-37	
			POUT = Prated_ETC, VBATT = VCC = 3.0 V, TCASE = TRANGE			-36	
	UTRA2	UTRA_ACLR2	POUT = Pratedsss		-43	-41	
			POUT = Prated_ETC, VBATT = VCC = 3.0 V, TCASE = TRANGE			-39	



Table 5-1. RR88643 Electrical Specifications for NTC – Transmit LTE Low Band

VBATT = 3.4 V; TCASE = +25 °C; LTE Signal = QPSK/10 MHz/12RB for MPR = 0 and QPSK/20 MHz/100RB for MPR = 1.

Parameter			Label	Conditions	Min	Typ	Max	Units
Modulation Accuracy			EVM	VBATT = 3.0 V to 4.8 V, Load = 50 ohms, TCASE = TRANGE		2.5	5.0	%
Harmonics	Second	Bands 12, 17, 28	2fo	POUT			-15	dBm
		Bands 5, 8, 13, 20, 26					-20	

Table 6. RR88643 Electrical Specifications for NTC – Transmit CDMA2000 Low Band

VBATT = 3.4 V; TCASE = +25 °C; 1x RC1

Parameter	Label	Conditions	Min	Typ	Max
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MB Electrical Spec:

Table 7. RR88643 Electrical Specifications for NTC –Transmit WCDMA Mid-Band

VBATT = 3.4 V; TCASE = +25 °C; Voice RMC 12.2 kbps

Parameter		Label	Conditions	Min	Typ	Max	Units
Frequency	Band 1	f_o		1920		1980	MHz
	Band 2			1850		1910	
	Band 3			1710		1785	
	Band 4			1710		1755	
Max Output Power	Band 1, 4	Prated		29.0			dBm
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	28			
	Bands 2, 3	Prated		29			
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	28.0			
Power Gain		GP_NTC	POUT = Prated	27.0		31.0	dB
		GP_ETC_B2,3,4	POUT = Prated, TCASE = TRANGE	26.0		32.5	
		GP_ETC_B1		25.5		32.5	
		GP_LOW	VCC = 0.55 V, POUT = 3 dBm			20.0	
Power Added Efficiency ¹		PAE_APT_B1	pout= Prated		41		%
		PAE_APT_B2,3,4			41		
Total Supply Current ²		I_TOT_MAX_B1	POUT = 28 dBm, VBATT = 3.8 V				mA
		I_TOT_MAX_B2,3	POUT = 28.5 dBm, VBATT = 3.8 V				
		I_TOT_MAX_B4	POUT = 28 dBm, VBATT = 3.8 V				
Adjacent Channel Leakage power Ratio	5 MHz offset	ACLR1	POUT = Prated		-40	-38	dBc
			POUT = Prated_ETC, VBATT = VCC = 3.0 V, TCASE = TRANGE			-36	
	10 MHz offset	ACLR2	POUT = Prated		-52	-48	
			POUT = Prated_ETC, VBATT = VCC = 3.0 V, TCASE = TRANGE			-46	
Modulation Accuracy		EVM	VBATT = 3.0 V to 4.8 V, Load = 50 ohms, TCASE = TRANGE		2.5	5.0	%
Harmonics	Second	$2f_o$				-15	dBm
	Third	$3f_o$				-20	
	Fourth and higher	$4f_o$				-20	
Noise Power in Rx Band at Duplex Frequency with WCDMA Modulated Tx							
B1 f_{TX} = 1920–1980 MHz	PNOISE_DPX		$f_{RX} = f_{TX} + 190$ MHz		-133.5		dBm/Hz
B2 f_{TX} = 1850–1910 MHz			$f_{RX} = f_{TX} + 80$ MHz		-133.0		
B3 f_{TX} = 1710–1785 MHz			$f_{RX} = f_{TX} + 95$ MHz		-132.5		
B4 f_{TX} = 1710–1755 MHz			$f_{RX} = f_{TX} + 400$ MHz		-137.0		
Input VSWR		VSWR_IN				2:1	VSWR
Stability		S	No oscillations, all spurious < -36 dBm, TRANGE	6:1			VSWR
Ruggedness		Ru	Prated, constant forward power, closed loop operation, VBATT = VCC = 4.8 V, TCASE = +25 °C	10:1			VSWR

¹ VCC optimized for ACLR1 = -40 dBc.

² $I_{TOT} = IBATT + (ICC1 + ICC2)(VCC/VBATT)(1/DC_DC_EFF)$. VCC ~ 3.1 V. DC_DC_EFF ~ 96%

	B25 $f_{TX} = 1850-1915$ MHz		$f_{RX} = f_{TX} + 80$ MHz		-131.5		
	B3 $f_{TX} = 1710-1785$ MHz		$f_{RX} = f_{TX} + 95$ MHz		-132.6		
	B4 $f_{TX} = 1710-1755$ MHz		$f_{RX} = f_{TX} + 400$ MHz		-136		
Input VSWR	VSWR_IN	P _{OUT}			1.3:1	2:1	VSWR
Stability	S	No oscillations, all spurious < -36 dBm, P _{OUT} BATT = 3.0 V to 4.8V, T					

Table 9. RR88643 Electrical Specifications for NTC – TD-SCDMA Bands 34, 39

VBATT = 3.4 V; TCASE = +25 °C; Voice Modulation

Parameter		Label	Conditions	Min	Typ	Max	Units
Frequency	Band 34	f_o		2010		2025	MHz
	Band 39			1880		1920	
Max Output Power		Prated		29			dBm
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	27.5			
Power Gain		GP_NTC	POUT = Prated	27.0		31.0	dB
		GP_ETC	POUT = Prated, TCASE = TRANGE	26.0		32.5	
		GP_LOW	VCC = 0.55 V, POUT = 3 dBm			20.0	dB
Power Added Efficiency ¹	Band 34	PAE_APT	POUT = Prated		39.5		%
	Band 39				42		
Total Supply Current ²		I_TOT_MAX_34	POUT = Prated, VBATT = 3.8 V				mA
		I_TOT_MAX_39					
Adjacent Channel Leakage Power Ratio	1.6 MHz offset	ACLR1	POUT = Prated		-40	-38	dBc
			POUT = Prated_ETC, VBATT = VCC = 3.0 V, TCASE = TRANGE			-36	
	3.2 MHz offset	ACLR2	POUT = Prated		-52	-48	
			POUT = Prated_ETC, VBATT = VCC = 3.0 V, TCASE = TRANGE			-46	
Modulation Accuracy		EVM	VBATT = 3.0 V to 4.8 V, Load = 50 ohms, TCASE = TRANGE		2.5	5.0	%
Harmonics	Second	$2f_o$	POUT			-13	dBm
	Third	$3f_o$				-20	
	Fourth and higher	$4f_o$				-20	
Input VSWR		VSWR_IN	POUT		1.3:1	2:1	VSWR
Stability		S	No oscillations, all spurious < -36 dBm, POUT Prated, VBATT = 3.0 V to 4.8 V, TCASE = TRANGE	6:1			VSWR
Ruggedness		Ru	No permanent damage to module. POUT Prated, constant forward power, closed loop operation,	10:1			VSWR
			VBATT = VCC = 4.8 V, TCASE = +25 °C				

¹ VCC optimized for ACLR1 = -40 dBc.

² $I_{TOT} = I_{BATT} + (I_{CC1} + I_{CC2})(V_{CC}/V_{BATT})(1/DC_DC_EFF)$. VCC ~ 3.1 V. DC_DC_EFF ~ 96%.



Table 11. RR88643 Electrical Specifications for NTC – Transmit CDMA2000 Mid-Band

VBATT = 3.4 V; TCASE = +25 °C; 1x RC1

Parameter		Label	Conditions	Min	Typ	Max	Units
Frequency	Band BC15	f_0		1710		1755	MHz
	Band BC4			1750		1780	
	Band BC1			1850		1910	
	Band BC6			1920		1980	
Max Output Power		Prated		28.5			dBm
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	27			
Power Gain	Bands BC6, BC15	GP_NTC	POUT = Prated	27.0		31.0	

dB

HB Electrical Spec:

Table 12. RR88643 Electrical Specifications for NTC – FDD LTE Band 7

VBATT = 3.4 V; TCASE = +25 °C; LTE Signal = QPSK/10 MHz/12RB for MPR = 0 and QPSK/20 MHz/100RB for MPR = 1.

Parameter		Label	Condition	Min	Typ	Max	Unit
Frequency		f_0		2500	2535	2537	MHz
Max Output Power		Prated	MPR = 0 ¹	28			dBm
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	27.5			
Gain		GP_NTC	POUT = Prated, TCASE = +25 °C	27.5			dB
		GP_ETC	POUT = Prated, TCASE = TRANGE	26		30	
		GP_LOW	POUT = 3 dBm, VCC = 0.55 V	13	15.5	20.0	
Power Added Efficiency ²		PAEAPT	POUT = Prated		32		%
Total Supply Current ³		I_TOT_MAX	POUT = Prated, VBATT = 3.8 V				mA
Adjacent Channel Leakage power	E-UTRA	E-UTRA_ACLR1	POUT = Prated		-38	-35	dBc
Ratio			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-33	
	UTRA1	UTRA_ACLR1	POUT = Prated		-40	-38	
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-35	
	UTRA2	UTRA_ACLR2	POUT = Prated		-43	-41	
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-39	
Harmonic Suppression	Second	$2f_0$	POUT		-25	-21	dBm
	Third	$3f_0$				-20	
	Fourth	$4f_0$				-26	
	Fifth	$5f_0$				-36	
Tx Noise in Rx Bands	Rx Band	PNRX_LTE	2620 MHz–2690 MHz ⁴			-126	dBm/Hz
	GPS Rx	PNRX_GPS	1574 MHz–1577 MHz ⁴			-140	
	BT, WLAN	PNRX_BT	2400 MHz–2452 MHz ⁴			-108	

Table 13. RR88643 Electrical Specifications for NTC – FDD LTE Band 30 (WCS)

VBATT = 3.4 V; TCASE = +25 °C; LTE Signal = QPSK/10 MHz/12RB for MPR = 0 and QPSK/10 MHz/50RB for MPR = 1.

Parameter		Label	Condition	Min	Typ	Max	Unit
Frequency		f_0		2305	2310	2315	MHz
Max Output Power		Prated	MPR = 0 ¹	28.2			dBm
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	27.5			
Gain		GP_NTC	POUT = Prated, TCASE = +25 °C	27.0	28.5	31.5	dB
		GP_ETC	POUT = Prated, TCASE = TRANGE	26.0		32.5	
		GP_LOW	POUT = 3 dBm, VCC = 0.55 V	13.0	15.3	20.0	
Power Added Efficiency ²		PAEAPT	POUT = Prated		33		%
Total Supply Current ³		I_TOT_MAX	POUT = Prated, VBATT = 3.8 V				mA
Adjacent Channel Leakage power	E-UTRA	E-UTRA_ACLR1	POUT = Prated		-38	-35	dBc
Ratio			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-33	
	UTRA1	UTRA_ACLR1	POUT = Prated		-40	-38	
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-35	
	UTRA2	UTRA_ACLR2	POUT = Prated		-43	-41	
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-39	
Harmonic Suppression	Second	2f ₀			-25	-15	dBm
	Third	3f ₀			-15	-14	
	Fourth	4f ₀				-21	
	Fifth	5f ₀				-36	
Tx Noise in Rx Bands	GPS Rx	PNRX_GPS	1574 MHz–1577 MHz ⁴			-140	dBm/Hz
	BT, WLAN	PNRX_BT	2400 MHz–2483.5 MHz ⁴			-113	
	WLAN	PNRX_5GHz	4900 MHz–5800 MHz ⁴			-140	
EVM		EVM	VBATT = 3.0 V to 4.8 V, Load = 50 ohms, TCASE = TRANGE		3	5	%
Input Voltage Standing Wave Ratio		VSWR				2: 1	
Stability (Spurious output)		S	6:1 VSWR All phases			-36	dBm
Ruggedness		Ru	No permanent damage to module.	10	1		VSWR
			power, closed loop operation,				
			LTE 10 MHz QPSK 12RB modulation, VBATT = VCC = 4.8 V, TCASE = +25 °C				

¹ MPR is the Max power reduction as defined in 3GPP TS36.101

² VCC optimized for ACLR1_EUTRA = -39 dBc, QPSK 10 MHz / 12RB.

³ I_TOT = IBATT + (ICC1

Efficiency							
Total Supply Current ³		I_TOT_MAX	POUT = Prated, VBATT = 3.8 V				mA
Adjacent Channel Leakage power Ratio	E-UTRA	E-UTRA_ACLR1	POUT = Prated		-38	-35	dBc
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-33	
		E-UTRA_ACLR1_CA	POUT = Prated, Modulation = QPSK/40 MHz/200RB		-37		
	UTRA1	UTRA_ACLR1	POUT = Prated		-40	-38	
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-35	
	UTRA2	UTRA_ACLR2	POUT = Prated		-43	-41	
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-39	
Harmonic Suppression	Second	2f ₀	POUT			-20	dBm
	Third	3f ₀				-16	
	Fourth	4f ₀				-26	
	Fifth	5f ₀				-36	
Tx Noise in Rx Bands	GPS Rx	PNRX_GPS	1574 MHz–1577 MHz ⁴			-140	dBm/Hz
	BT, WLAN	PNRX_BT	2400 MHz–2483.5 MHz ⁴			-113	
	WLAN	PNRX_5GHz	4900 MHz–5800 MHz ⁴			-140	
EVM		EVM	VBATT = 3.0 V to 4.8 V, Load = 50 ohms, TCASE = TRANGE		3	5	%
Input Voltage Standing Wave Ratio		VSWR				2: 1	
Stability (Spurious output)		S	6:1 VSWR All phases			-36	dBm

Ruggedness – no damage Ru No permanent damage to module. POUT power, closed loop operation, 10 1 VSWR

Table 15. RR88643 Electrical Specifications for NTC – TDD Band 40

VBATT = 3.4 V; TCASE = +25 °C; LTE Signal = QPSK/10 MHz/12RB for MPR = 0 and QPSK/20 MHz/100 RB for MPR = 1.

Parameter		Label	Condition	Min	Typ	Max	Unit
Frequency		f_0		2300	2350	2400	MHz
Max Output Power		Prated	MPR = 0 ¹	28			dBm
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	27.5			
Gain		GP_NTC	POUT = Prated, TCASE = +25 °C	28			dB
		GP_ETC	POUT = Prated, TCASE = TRANGE	27.0		31.0	
		GP_LOW	POUT = 3 dBm, VCC = 0.55 V	13.0	15.0	20.0	
Power Added Efficiency ²		PAEAPT	pout= Prated		33		%
Total Supply Current ³		I_TOT_MAX	pout= Prated, VBATT = 3.8				mA
Adjacent Channel Leakage power Ratio	E-UTRA	E-UTRA_ACLR1	POUT = Prated		-38	-35	dBc
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-33	
		E-UTRA_ACLR1_CA	POUT = Prated, Modulation = QPSK/40 MHz/200RB		-37		
	UTRA1	UTRA_ACLR1	POUT = Prated		-40	-38	
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-35	
	UTRA2	UTRA_ACLR2	POUT = Prated		-43	-41	
Harmonic Suppression	Second	2f ₀	POUT			-21	dBm
	Third	3f ₀				-16	
	Fourth	4f ₀				-21	
	Fifth	5f ₀				-36	
Tx Noise in Rx Bands	GPS Rx	PNRX_GPS	1574 MHz–1577 MHz ⁴			-140	dBm/Hz
	BT, WLAN	PNRX_BT	2447 MHz–2483.5 MHz ⁴			-104	
	WLAN	PNRX_5GHz	4900 MHz–5800 MHz ⁴			-140	
EVM		EVM	VBATT = 3.0 V to 4.8 V, Load = 50 ohms, TCASE = TRANGE		3	5	%
Input Voltage Standing Wave Ratio		VSWR				2: 1	
Stability (Spurious output)		S	6:1 VSWR All phases			-36	dBm
Ruggedness – no damage		Ru	No permanent damage to module. POUT power, closed loop operation, LTE 10 MHz QPSK 12RB modulation, VBATT = VCC = 4.8 V, TCASE = +25 °C	10	1		VSWR

¹ MPR is the Max power reduction as defined in 3GPP TS36.101

² VCC optimized for ACLR1_EUTRA = -39 dBc, QPSK 10 MHz / 12RB.

³ I_TOT = IBATT + (ICC1 + ICC2)(VCC/VBATT)(1/DC_DC_EFF). VCC ~ 3.4 V, DC_DC_EFF ~ 96%.

⁴ Measured with 20 MHz/100RB LTE Wave form.

Table 16. RR88643 Electrical Specifications for NTC – TDD Band 41, TDD AXGP Band

VBATT = 3.4 V; TCASE = +25 °C; LTE Signal = QPSK/10 MHz/12RB for MPR = 0 and QPSK/20 MHz/100 RB for MPR = 1.

Parameter		Label	Condition	Min	Typ	Max	Unit	
Frequency		f_0		2496	2595	2690	MHz	
Max Output Power		Prated	MPR = 0 ¹	28			dBm	
		Prated_ETC	VBATT = VCC = 3.0 V, TCASE = TRANGE	27.5				
Gain		GP_NTC	POUT = Prated, TCASE = +25 °C	29.0		30	dB	
		GP_ETC	POUT = Prated, TCASE = TRANGE	27.5		31.0		
		GP_LOW	POUT = 3 dBm, VCC = 0.55 V	13.0	15.0	20.0		
Power Added Efficiency ²		PAEAPT	POUT = Prated		32		%	
Total Supply Current ³		I_TOT_MAX	POUT = Prated, VBATT = 3.8 V				mA	
Adjacent Channel Leakage power Ratio	E-UTRA	E-UTRA_ACLR1	POUT = Prated		-38	-35	dBc	
			POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE			-33		
		E-UTRA_ACLR1_CA	POUT = Prated, Modulation = QPSK/40 MHz/200RB		-37			
		UTRA1	UTRA_ACLR1	POUT = Prated	-40	-38		
				POUT = Prated_RANGE, VBATT = VCC = 3.0 V, TCASE = TRANGE				-35
		UTRA2	UTRA_ACLR2	POUT = Prated	-43	-41		
Harmonic Suppression	Second	$2f_0$	POUT			-18	dBm	
	Third	$3f_0$				-16		
	Fourth	$4f_0$				-26		
	Fifth	$5f_0$				-36		
Tx Noise in Rx Bands	GPS Rx	PNRX_GPS	1574 MHz–1577 MHz ⁴			-140	dBm/Hz	
	BT, WLAN	PNRX_BT	2400 MHz–2452 MHz ⁴			-104		
	WLAN	PNRX_5GHz	4900 MHz–5800 MHz ⁴			-140		
EVM		EVM	VBATT = 3.0 V to 4.8 V, Load = 50 ohms, TCASE = TRANGE		3	5	%	
Input Voltage Standing Wave Ratio		VSWR				2: 1		
Stability (Spurious output)		S	6:1 VSWR All phases			-36	dBm	
Ruggedness – no damage		Ru	No permanent damage to module. POUT constant forward power, closed loop operation,	10	1		VSWR	
			LTE 10 MHz QPSK 12RB modulation,					
			VBATT = VCC = 4.8 V, TCASE = +25 °C					

¹ MPR is the Max power reduction as defined in 3GPP TS36.101

² VCC optimized for ACLR1_EUTRA = -39 dBc, QPSK 10 MHz / 12RB.

³ I_TOT = IBATT + (ICC1 + ICC2)(VCC/VBATT)(1/DC_DC_EFF). VCC ~ 3.4 V, DC_DC_EFF ~ 96%.

⁴ Measured with 20 MHz/100RB LTE Wave form.

Table 17. RR88643 Electrical Specifications for NTC – Band Select Switch

Parameter		Label	Conditions	Min	Typ	Max	Unit
Frequency Range		f_{LB}		699		915	MHz
		f_{MB}		1710		1980	
		f_{HB}		2300		2690	
Insertion Loss		IL	HB1 to TXRx2		0.70		dB
			HB2 to TXRx2		0.75		
			HB3 to TXRx1		0.75		
			HB4 to TXRx2		0.75		
VSWR		SWR	Any RF port tested in Rx mode			1.8	1
Isolation	Tx Mode	ISO_Tx	Tx output at HB1, Isolation to HB2	30	46		dB
			Tx output at HB1, Isolation to HB3, HB4, TXRX1	32	34.5		
			Tx output at HB1, Isolation to TXRX2	25	27		
			Tx output at HB2, Isolation to HB1, HB3, HB4, TXRX1	30	34		
			Tx output at HB2, Isolation to TXRX2	25	27		
			Tx output at HB3, Isolation to HB1, HB2, HB4, TXRX1	28	29.4		
			Tx output at HB3, Isolation to TXRX2	22	24		
			Tx output at HB4, Isolation to HB2, HB3	30	34.9		
			Tx output at HB4, Isolation to TXRX1	28	31		
			Tx output at HB4, Isolation to HB1, TXRX2	22	23.5		
			Tx output at any LB output, Isolation to any other LB output	35			
			Tx output at any MB output, Isolation to MB1, MB2, MB3, MB4	30			

EVB Description:

The evaluation board is a platform for testing and interfacing design circuitry. To accommodate the interface testing of the RR88643-21, the evaluation board schematic and assembly diagrams are included for analysis and design. The basic EVB schematic is shown in Figure 2 and the assembly diagram in Figure 3. Table 21 is the Bill of Material list.

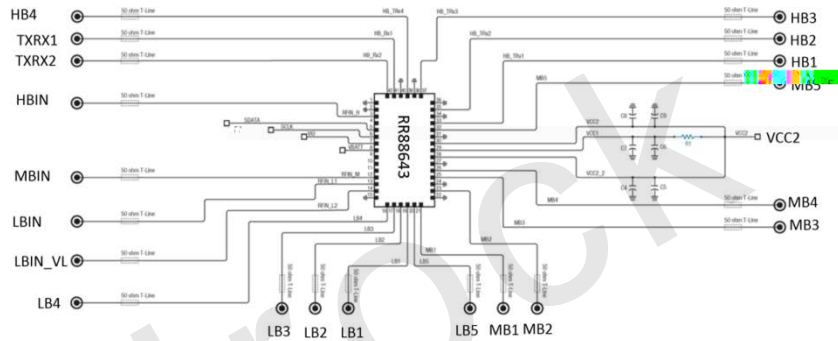


Figure 2. RR88643 Evaluation Board Schematic Diagram

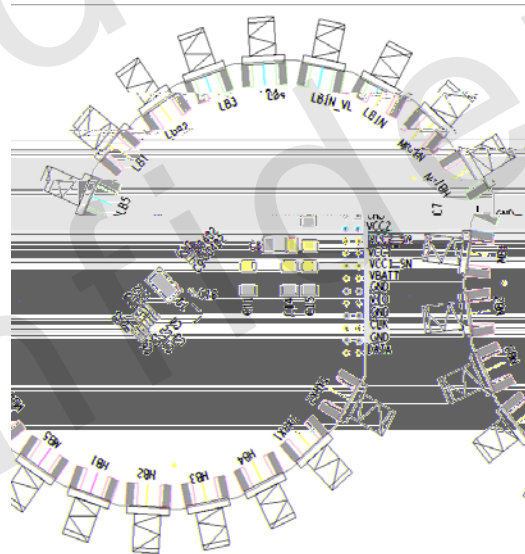


Figure 3. RR88643 Evaluation Board Assembly Diagram

Table 18. RR88643 Evaluation Board Bill of Material

	QTY	REFERENCE DESIGNATORS	PART DESCRIPTION
1	1	J1	CONNECTOR, 12 PINPOST LENGTH = 0.53
2	20	HB1,HB2,HB3,HB4,MB1,MB2,MB3,MB4,MB5, LB1, LB2, LB3, LB4, LB5, TXRX1, TXRX2, LBIN, L BIN_VL, MBIN, HBIN	CONN SMA END LAUNCH JACK TAB CONTACT GOLD .062
3	1	R2	RESISTOR, 0 OHM, JUMPER, 0.063 W, 0402
4	1	C3	CAPACITOR, CERAMIC, 100nf, 10%, 10 V, 0402
5	1	C4	CAPACITOR, CERAMIC, 1nf, 10%, 10 V, 0402
6	1	C5	CAPACITOR, CERAMIC, 10uf, 10%, 10 V, 0402
7	1	C6	CAPACITOR, CERAMIC, 2.2uf, 10%, 10 V, 0603
8	1	C10	CAPACITOR, CERAMIC, 100nf, 10%, 10 V, 0603
9	1	C12	CAPACITOR, CERAMIC, 0.1uf, 10%, 10 V, 0402
10	1	C13	CAPACITOR, CERAMIC, 100pf, 10%, 10 V, 0402
11	5	C7,C8,C11,C14,C15	CAPACITOR 470 μ F, TANT, LOW ESR, CASE D, AVX
12	5	R1,C1,C2,C9,C18	Do Not Place

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Package Mechanical Drawing:

Figure 4 is a mechanical drawing of the pad layout for the RR88643-21, a 42-pad leadless quad-band power amplifier module. Figure 5 provides a recommended PC board layout footprint of the module to help the designer attain optimum thermal conductivity, good grounding, and Min RF discontinuity for the 50-ohm terminals.

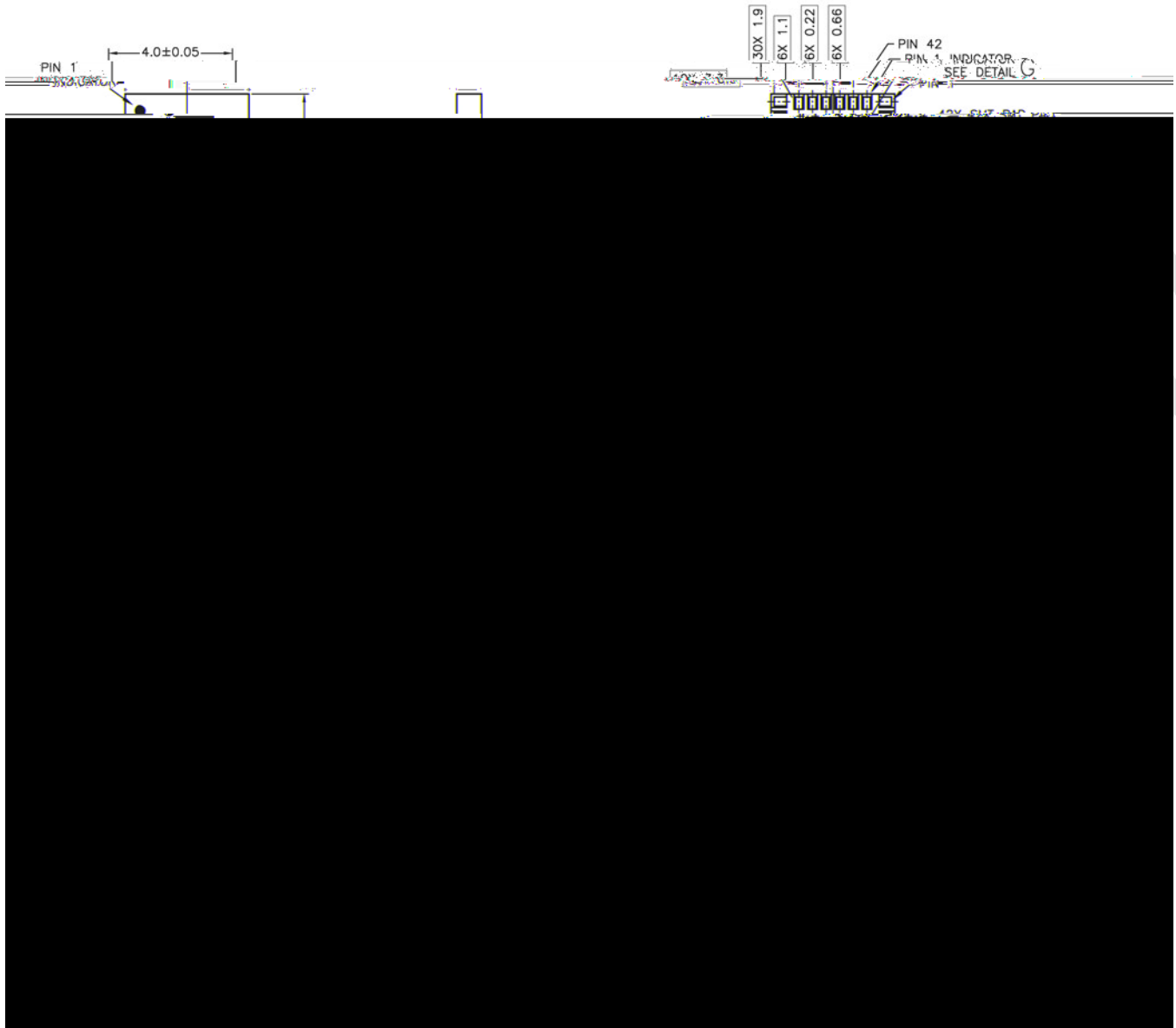


Figure 4. Dimensional Diagram for 4.0 mm x 6.8 mm x 0.75 mm, 42-Pad MCM Package – RR88643

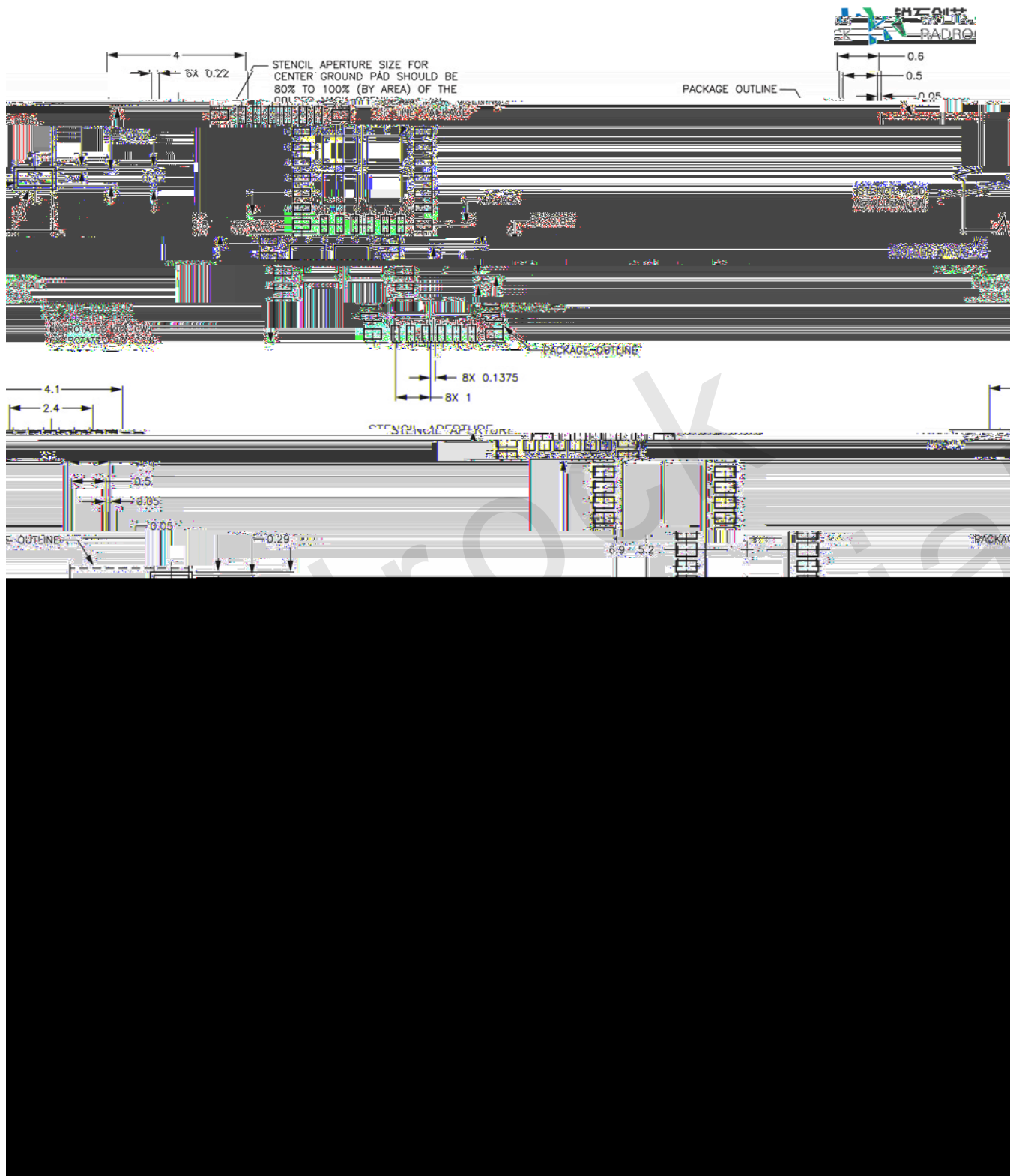


Figure 5. PCB Layout Footprint for 4.0 mm x 6.8 mm, 42-Pad Package – RR88643

